

# Silver-epoxy microwave filters and thermalizers for millikelvin experiments

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We present silver-epoxy filters combining excellent microwave attenuation with efficient wire thermalization, suitable for low temperature quantum transport experiments. Upon minimizing parasitic capacitances, the attenuation reaches  $\geq 100$  dB above  $\approx 150$  MHz and—when capacitors are added—already above  $\approx 30$  MHz. We measure the device electron temperature with a GaAs quantum dot and demonstrate excellent filter performance. Upon improving the sample holder and adding a second filtering stage, we obtain electron temperatures as low as  $7.5 \pm 0.2$  mK in metallic Coulomb blockade thermometers. © 2014 Author(s). All article content, except where otherwise noted, is licensed under a Creative Commons Attribution 3.0 Unported License.

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Advancing to lower system temperatures is of fundamental interest, allowing to resolve smaller energy scales and hence opening the door for new physics. Examples include fractional quantum hall states,<sup>1–4</sup> nuclear spin phase transitions,<sup>5–8</sup> and many more. Dilution refrigerators with base temperatures as low as 5 mK are readily available, but typical electron temperatures  $T_e$  in semiconductor nanoscale devices are often considerably higher. This is due to insufficient thermalization, absence of adequate filtering of high frequency radiation<sup>9,10</sup> as well as low frequency noise from the measurement setup including ground loops.

To attenuate microwave radiation, various types of cryogenic filters have been developed, such as metal powder filters,<sup>11–14</sup> micro fabricated filters,<sup>15–18</sup> thermo-coax cables,<sup>19,20</sup> copper tapeworm filters,<sup>21,22</sup> thin film filters,<sup>23</sup> and lossy transmission lines.<sup>24</sup> An overview of various microwave filters is given in Ref. 25. Despite decades of research, the state of the art is not satisfactory for cooling at 10 mK to below. Besides filtering strategies, reduction of internal heat leaks and proper thermalization for sample, sample holder and wires are equally important. We note that the electron-phonon coupling has a very strong temperature dependence,<sup>2,26</sup> scaling as  $T^5$ , making low temperature cooling very challenging. Wiedemann-Franz cooling<sup>26</sup> through the electrical sample wires scales much more favorably, as  $T^2$ , and eventually becomes the dominant cooling mechanism.<sup>2</sup>

In this Letter, we present Ag-epoxy cryogenic filters uniting excellent microwave attenuation with efficient thermalization, suitable for low temperature electronic measurements. The filters are modular, of small size, robust against thermal cycling, and possess a predictable attenuation spectrum which can be understood as a skin-effect filter in a lossy transmission line model. Thermometry experiments using a GaAs quantum dot and metallic Coulomb blockade thermometers (CBTs) are employed to investigate the filter

performance, demonstrating CBT electron temperatures<sup>27</sup> down to  $7.5 \pm 0.2$  mK.

The filters are made by neatly winding 2.5 m of insulated Cu-wire (diameter  $D = 0.1$  mm) with a magnet winding machine around a precast Ag-epoxy<sup>28</sup> rod to form a 5-layer coil. While winding, we continuously wet the rod and wires with Ag-epoxy to embed the wire in a conducting matrix, ensuring full electrical encapsulation and good thermal contact of the wire throughout the filter. Standard micro coaxial connectors are soldered and, after gluing parts together with insulating epoxy,<sup>29</sup> a high-conductivity Cu-braid is fastened to the filter also with Ag-epoxy, serving as a thermal anchor. A completed filter is shown in Fig. 1 (inset). We mount the filters at the mixing chamber (MC) of a dilution refrigerator with  $T_{MC} \approx 5$  mK, at base temperature. We emphasize that the wire is tightly embraced in the Ag-epoxy matrix, which is a good

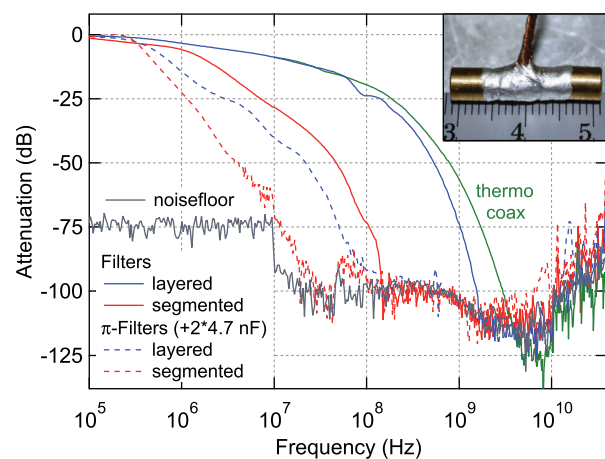


FIG. 1. Attenuation of thermo-coax and various filters at room temperature. Blue and red represent layered and segmented filters, respectively, with DC resistance  $\approx 5 \Omega$  ( $< 50$  m $\Omega$  at 4.2 K) and capacitance  $\approx 4$  nF. The dashed curves are from  $\pi$ -type filters with 4.7 nF added at each end. The jump in the noise floor at 10 MHz is due to use of a different analyzer at frequencies  $\geq 10$  MHz. Cooling to 4.2 K does not significantly alter the attenuation. Inset: photo of a filter (22 mm long, 5 mm diameter) with centimeter scale bar.

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electrical conductor down to the lowest  $T_{MC}$ . This is facilitating both cooling of the wire through the  $d_{ins.} \sim 8 \mu\text{m}$  thick polyurethane insulation as well as high frequency filtering—thus presenting an advantage over earlier designs.

The attenuation profile is shown in Fig. 1, providing an attenuation  $\geq 100$  dB above 1.5 GHz (solid blue trace). Parasitic or stray capacitances can severely degrade the high-frequency attenuation. An optimized filter layout can reduce such capacitances: we split-up the coil into separate smaller segments arranged in series, resulting in a segmented filter, as opposed to the layered filter previously described. For layered filters, the first and the last layers of the coil are relatively close, separated only by 3 layers of Cu-wire or equivalently 0.3 mm distance. In contrast, the first and last coils are at a distance of  $\approx 5$  mm for the segmented filters. In a naive plate capacitor model, this corresponds to a reduction of capacitive coupling by a factor of  $\sim 10$ . Indeed, we obtain 100 dB or more already above 150 MHz for the segmented filters (red solid trace, Fig. 1), at 10 times lower frequencies compared to the layered filter.

The filter attenuation can be modeled as a lossy, distributed transmission line with skin-effect: the resistance per length  $R_{IC}$  and  $R_{OC}$  of inner and outer conductor, respectively, depend on frequency. While DC-currents are supported by the whole cross section  $A = \pi D^2/4$  of the Cu-wire with diameter  $D$ , the skin effect forces AC-currents at frequency  $\nu$  to an annulus of width  $\delta = 1/\sqrt{\sigma_{Cu}\mu_{Cu}\nu\pi}$ —the skin depth. Here,  $\sigma_{Cu}$  denotes the conductivity and  $\mu_{Cu}$  the magnetic permeability of the Cu-wire. The effective cross section therefore reduces to  $A = D\pi\delta$ , and consequently  $R_{IC}$  increases as a function of  $\nu$ . Equivalent arguments hold for the outer conductor. Note that due to its smaller conductivity  $\sigma_{epoxy} \ll \sigma_{Cu}$ , the Ag-epoxy outer conductor is dominating the total resistance  $R_{tot}(\nu) = R_{IC}(\nu) + R_{OC}(\nu)$  at high  $\nu \geq 2$  MHz.

The resulting attenuation is  $\propto -\nu^{3/4}$  for low frequencies  $2\pi\nu L \ll R_{tot}(\nu)$  and  $\propto -\nu^{1/2}$  for high frequencies in the GHz regime with a smooth transition in between. Here,  $L = \mu_0/2\pi \ln((D + 2d_{ins.})/D)$  denotes the inductance per length. For a single layer filter, where additional parasitic capacitances not included in the theory are negligible, the model is in very good agreement with the measured attenuation without using any fit parameters, see supplementary material<sup>30</sup> for details. This demonstrates the validity of the skin-effect lossy transmission line model and can explain the general functional shape of the measured filter attenuation curves. When comparing the model with the segmented and layered filters described above, the agreement between theory and measured attenuation is still good once a fit parameter is allowed, e.g., the capacitance per length.

A  $\pi$ -type filter can be created by adding capacitors<sup>31</sup> to both filter ends, further increasing the attenuation.<sup>14</sup> Both layered and segmented  $\pi$ -filters are shown in Fig. 1 as dashed curves, delivering an attenuation  $\geq 100$  dB attenuation already above 30 MHz for segmented  $\pi$ -type filter. However, in order not to reduce the bandwidth for future experiments too much and because of increased current preamplifier noise with large capacitances, the segmented filters without additional capacitors were chosen for the electron temperature measurements. Further, we note that replacing the central

rod with a mixture of insulating epoxy<sup>29</sup> and Fe powder<sup>14</sup> or replacing the copper wire with resistive wire did not improve the filter performance. Once properly completed, repeated thermal cycling has not yet caused failure of any of the over hundred filters in use in our laboratory.

We now turn to the filter performance measured by the device electron temperature  $T_e$  with a GaAs quantum dot thermometer,<sup>32–40</sup> comparing  $T_e$  with and without filters. For all measurements below, the sample wires are connected to the room temperature measurement setup through  $\sim 1.5$  m long thermo-coax cables,<sup>41</sup> which are very effective attenuators above a few GHz, see green curve in Fig. 1, up to very high frequencies  $\approx 1$  THz.<sup>19</sup> The Ag-epoxy filters provide additional thermalization and serve to significantly bring down the low-pass cut-off frequency of the combined thermo-coax and Ag-epoxy filter system.

We use a surface gated GaAs/AlGaAs quantum dot (see Fig. 2, lower inset) in the temperature broadened Coulomb blockade regime as an electron thermometer, probing the Fermi-Dirac distribution of the reservoirs. Gates N, L, R and P isolate the quantum dot from the surrounding 2D gas, while source (S) and drain (D) ohmic contacts serve for current injection and detection. Details on sample fabrication are given in Ref. 42. All measurements presented here were performed in the single electron regime. The dot differential conductance  $g$  at zero DC-bias peaks whenever one of its energy levels is aligned with the chemical potential of source and drain, see upper inset, Fig. 2. The shape and width  $\Delta V_G$  in gate voltage of this peak reflects the reservoir temperatures and provides a primary electron thermometer<sup>32–34</sup> in the limit of small tunnel rates to source and drain (and large dot level spacing) compared to temperature. The conversion between measured peak width  $\Delta V_G$  and energy  $\Delta E$  is done by means of the lever arm  $\alpha$ ,  $\Delta E = \alpha \Delta V_G$ . To determine  $\alpha$ , a DC-bias  $eV_{SD} \gg k_B T_e$  is applied (Boltzmann constant  $k_B$  and

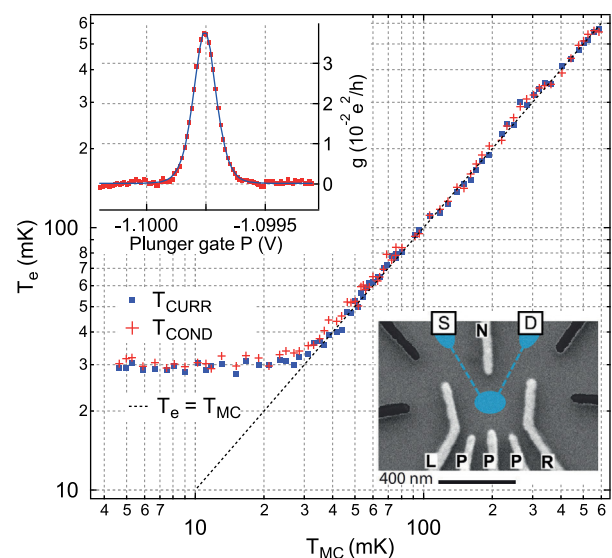


FIG. 2. Electron temperature  $T_e$  extracted from GaAs dot current and conductance measurements as indicated versus refrigerator temperature  $T_{MC}$ , with dashed line showing ideal thermalization  $T_e = T_{MC}$ . The upper inset shows a typical zero-bias peak at base temperature with  $\cosh^{-2}$  fit.<sup>33</sup> Lower inset: micrograph of a similar device. Gates N, L, R, and P (light grey) form the dot. Dark gates are grounded.

electron charge  $e > 0$ ), splitting the zero-bias conductance peak into two peaks with gate-voltage separation  $V_{\text{sep}}$  and giving  $\alpha = eV_{\text{SD}}/V_{\text{sep}}$ . All conductance data are recorded with a standard lock-in technique using an AC-excitation experimentally chosen sufficiently small to avoid heating ( $2 \mu\text{V}$  at base temperature).

Instead of differential conductance  $g$  at  $V_{\text{SD}} = 0$ , we can also measure the DC dot current  $I_{\text{DC}}$  at large bias  $eV_{\text{SD}} \gg k_B T$ , which shows transitions between low and high current when a dot level is crossing the chemical potential of source and drain. These current transitions reflect the Fermi-Dirac distribution of the corresponding reservoir and allow extraction of source and drain temperature separately (see Ref. 40 for details). Here,  $\alpha$  is simply given by the ratio of the applied bias  $V_{\text{SD}}$ , divided by the separation of the inflection points for the two distributions. We stress that  $I_{\text{DC}}$  is determined by the tunneling rates and does not depend on the applied DC voltage as long as excited states (and cotunneling) do not contribute, as we assume here. Also, reducing the DC bias did not affect  $T_e$ . Further, there is no systematic deviation between source and drain temperatures, and we conclude that energetic electrons at  $\geq 100 \mu\text{eV}$  become efficiently thermalized in the reservoirs.

Fig. 2 displays the extracted electron temperatures from the same warmup using both  $g$  and  $I_{\text{DC}}$ . We measure the zero-bias  $g$ -peak with a finite AC excitation  $V_{\text{AC}}$  and then the  $I_{\text{DC}}$  steps at finite  $V_{\text{SD}}$ , and repeat this cycle. We perform Fermi-Dirac distribution fits on the  $I_{\text{DC}}$  steps, forcing the two reservoir temperatures to be identical in order to obtain more reliable fitting results and obtain the reservoir electron temperature  $T_{\text{CURR}}$  as well as the lever arm  $\alpha$ . Finally, we get the reservoir electron temperature  $T_{\text{COND}}$  from  $\cosh^{-2}$  fits<sup>32–34</sup> to the  $g$ -peak using the same lever arm. We note that for the  $g$  measurements, already a small  $eV_{\text{DC}} \sim k_B T$  results in a broadened peak and an overestimation of  $T_{\text{COND}}$ . Therefore, a feedback mechanism was implemented that compensates any  $V_{\text{SD}}$  drift by minimizing  $I_{\text{DC}}$  in the zero-bias measurement, therefore ensuring excellent alignment of source and drain during the  $\sim 40$  h measurement shown in Fig. 2.

Both electron temperatures  $T_{\text{CURR}}$  and  $T_{\text{COND}}$  agree well with each other over the whole temperature range and, above 40 mK, also with  $T_{\text{MC}}$ , measured with a  $\text{RuO}_2$  thermometer, thus demonstrating excellent thermometer operation. The  $\text{RuO}_2$  thermometer was precalibrated with a fixed point device and agrees very well with a Cerium Magnesium Nitrate paramagnetic inductance thermometer.<sup>26</sup> Below 40 mK,  $T_e$  starts deviating from  $T_{\text{MC}}$  and saturates at  $\sim 30$  mK. Upon reducing electronic noise and simplifying the measurement setup (measuring only  $I_{\text{DC}}$ ), the electron temperature  $T_e$  is reduced down to  $T_{\text{CURR}} = 18 \pm 3$  mK, obtained from an average over several traces. An example of such a trace including Fermi-Dirac fits of the current steps and individually extracted source/drain temperatures is presented in the upper panel of Fig. 3.

When the filters are removed (replaced with plain adaptors) without changing any other part of the experiment, the electron temperature increases to  $T_{\text{CURR}} \approx 75$  mK, see lower panel of Fig. 3, clearly demonstrating the efficiency of the filters. Note that due to the elevated electron temperature, more DC bias is applied to better separate the source and drain current steps. However, this does not increase the

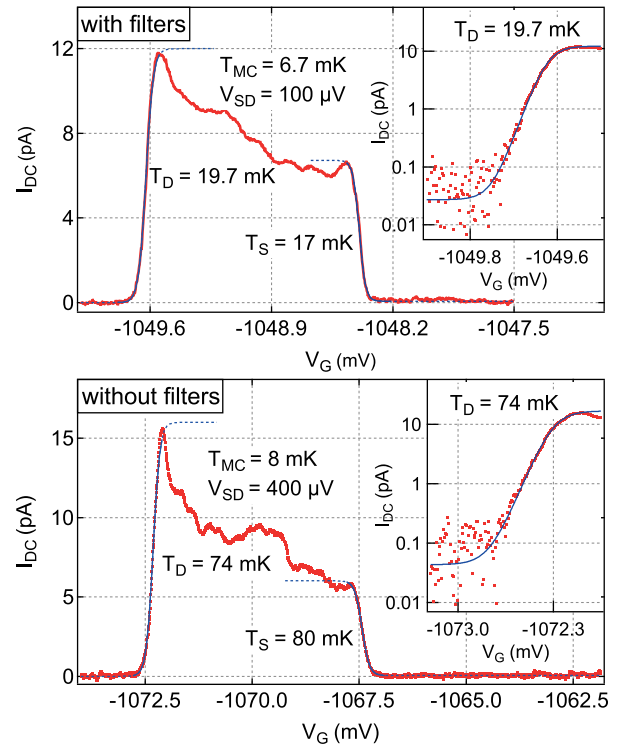


FIG. 3. DC current  $I_{\text{DC}}$  through the GaAs dot as a function of plunger gate voltage  $V_G$  taken at base temperature, with individual Fermi-Dirac fits for source and drain leads, as indicated, comparing data with filters (upper panel) and without filters (lower panel). The insets show the same data and fit in log-scale for the drain side. The linear current increase seen here clearly indicates the temperature broadened regime.

electron temperature, as discussed already earlier (also here, smaller bias was tested).

There is still room for further improving the electron temperature, considering that the refrigerator cools to  $T_{\text{MC}} = 5$  mK. An additional miniaturized Ag-epoxy filtering/thermalization stage is placed directly inside the Faraday cup, i.e., inside the shielded sample can. Furthermore, a heat sunk sample holder made from conductive Ag-epoxy is used, and the ceramic chip carrier (known to suffer from heat release) is replaced with a standard plastic dip socket equipped with a 1 mm thick gold plated and heat sunk copper backplane. We proceed to measure electron temperatures with this improved two stage filtering/thermalization setup. For simplicity, metallic CBTs are used that—in contrast to GaAs dots—do not require gate tuning to deliver the device electron temperature. We note that CBTs have cooled to very similar electron temperatures as the dot before improving the setup.

The CBTs consist of 7 parallel arrays with 64 angle evaporated tunnel junctions between metal islands,<sup>43,44</sup> operating in the regime where the charging energy  $E_C$  is comparable to temperature<sup>45</sup> (in contrast to the dot measurements in Figs. 2 and 3, where  $E_C \gg k_B T_e$ ). The full width at half minimum of the  $V_{\text{SD}}$  dependent conductance dip (see inset of Fig. 4) can serve as a primary thermometer.<sup>46,47</sup> Here, however, we use the conductance dip  $\Delta g/g$  as a secondary thermometer to avoid heating at finite bias.<sup>27</sup> At high temperatures, where the CBTs are in thermal equilibrium with the refrigerator,  $E_C$  can be extracted from the temperature dependent  $\Delta g/g$  using the relation<sup>45</sup>  $\Delta g/g = u/6 - u^2/60 + u^3/630$ , where  $u = E_C/k_B T_{\text{CBT}}$ .

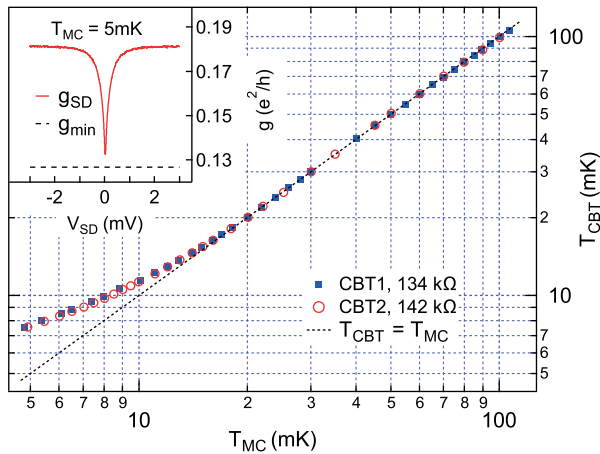


FIG. 4. CBT electron temperature  $T_{\text{CBT}}$  versus mixing chamber temperature  $T_{\text{MC}}$  for two CBTs on the same wafer, measured in a perpendicular magnetic field of 250 mT to drive the Al normal. Ideal thermalization  $T_{\text{CBT}} = T_{\text{MC}}$  is indicated as a dashed line. The inset shows  $g$  as a function of  $V_{\text{SD}}$  at base temperature (red), exhibiting a zero-bias conductance dip. The minimum conductance  $g_{\text{min}}$ , measured while waiting at  $V_{\text{SD}} = 0$  for an extended time, is indicated as dashed line—well below  $g(V_{\text{SD}} = 0)$  when sweeping.

With  $E_{\text{C}}$  obtained in this way, the measured  $\Delta g/g$  can be converted to the corresponding electron temperature  $T_{\text{CBT}}$  in the full temperature range.

Fig. 4 shows  $T_{\text{CBT}}$  as a function of refrigerator temperature  $T_{\text{MC}}$  for two CBTs, measured using standard lock-in technique with AC excitation of  $4 \mu\text{V}$  (experimentally chosen small enough to avoid heating effects). At the lowest temperatures, the zero-bias conductance  $g(V_{\text{SD}} = 0)$  drops over a time span of several minutes and eventually saturates at a value  $g_{\text{min}}$ , clearly lower than the minimum conductance from  $V_{\text{SD}}$  sweeps, see inset of Fig. 4. We therefore use  $g_{\text{min}}$  in order to obtain the conductance dip  $\Delta g/g$  and consequently the electron temperature  $T_{\text{CBT}}$ . The extracted electron temperatures  $T_{\text{CBT}}$  from both devices (circles and squares) agree very well with each other and with the refrigerator temperature  $T_{\text{MC}}$  down to about 15 mK, where the electron temperatures begin to saturate, eventually reaching a minimum<sup>27</sup>  $T_{\text{CBT}} = 7.5 \pm 0.2$  mK for  $T_{\text{MC}} \approx 5$  mK.

We note that fitting the data in Fig. 4 with a power law<sup>27</sup>  $T_{\text{CBT}}^p = T_{\text{S}}^p + T_{\text{MC}}^p$  gives  $p = 2.7 \pm 0.2$  and  $T_{\text{S}} = 6.9 \pm 0.1$  mK, where  $T_{\text{S}}$  is a theoretical  $T_{\text{MC}} = 0$  K saturation limit of  $T_{\text{CBT}}$ . This exponent  $p$  is clearly lower than  $p = 5$  which corresponds to electron-phonon coupling,<sup>26</sup> thus indicating an additional cooling mechanism stronger than electron-phonon cooling at low temperatures, presumably electronic (i.e., Wiedemann-Franz<sup>26</sup>) cooling.<sup>27</sup> Nevertheless, if only electron-phonon coupling is assumed, the lowest electron temperature reached corresponds to a residual heat leak of 13 aW per junction.<sup>27,43</sup>

In conclusion, we have presented miniature microwave filters uniting superior attenuation with efficient wire thermalization. The attenuation reaches  $\geq 100$  dB above 150 MHz or, when capacitors are added, already above 30 MHz. The filters are modular, of small size, robust against thermal cycling, and the attenuation is well understood by skin-effect filtering in a lossy transmission line model. Combined with thermo-coaxes, one stage of Ag-epoxy filters reduces the electron temperature, measured with a GaAs quantum dot, from

$\approx 75$  mK down to  $\approx 18$  mK. With an improved setup comprising, a second Ag-epoxy filtering stage and a better chip holder, electron temperatures as low as  $7.5 \pm 0.2$  mK are obtained in metallic CBTs.

Further experiments are required to determine whether the second filter stage or the improved chip carrier (or both) are necessary for the improvement from 18 mK to 7.5 mK. Finally, we point out that the filtering strategy introduced here was recently used to find evidence for helical nuclear spin order in GaAs quantum wires<sup>8</sup>—thus showing that the extensive filtering and thermalization introduced here can be important for studying new physics.

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