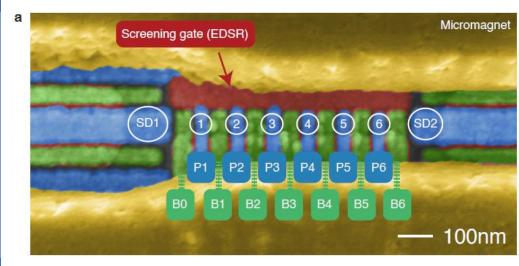
### Universal control of a six-qubit quantum processor in silicon

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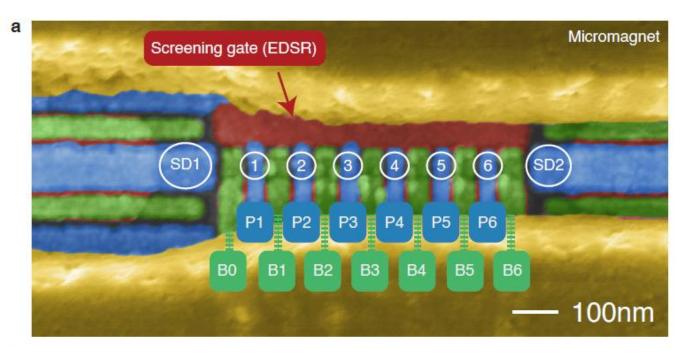
<b>5</b>	<b>-</b>	<u></u>			
Qubit	T <sub>2</sub> *(us)	T <sub>2</sub> <sup>h</sup> (us)	Vis(%)		
1	3.0±0.2	14.0 <b>±</b> 3.2	95.5		
2	2.5±0.1	21.1 <b>±</b> 6.7	95.5		
3	3.7±0.3	40.1 <b>±</b> 4.5	98		
4	3.7±0.3	37.2 <b>±</b> 3.8	97		
5	5.9 <b>±</b> 0.4	44.7 <b>±</b> 8.2	93.5		
6	5.1 <b>±</b> 0.4	26.7 <b>±</b> 4.8	96.5		
d $Q_1 : 99.77\% \pm 0.04$ $Q_2 : 99.87\% \pm 0.02$ $Q_3 : 99.96\% \pm 0.01$ $Q_4 : 99.88\% \pm 0.02$ $Q_5 : 99.91\% \pm 0.02$ $Q_5 : 99.91\% \pm 0.02$ $Q_6 : 99.88\% \pm 0.01$ 0.1 0 100 200 300 400 nth Clifford (#)					

,	Qubits	Fidelity (%)	Concurence (%)	
	1-2	89.2 ± 2.2	86.7 ± 3.2	
	2-3	90.1 ± 2.2	83.9 ± 3.8	
	3-4	88.3 ± 3.6	87.9 ± 5.0	
	4-5	95.6 ± 2.0	94.9 ± 3.2	
	5-6	94.1 ± 1.4	90.6 ± 3.6	

h

Felix J. Schupp 28/02/2022

## <u>Device</u>



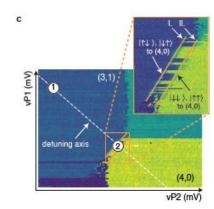
- 6 dots can accumulate with 1e<sup>-</sup>, but choose to accumulate outer dots (1,6) with 3 electrons
- 2 sensor dots on end of array
- Approach:

screening ("slit") gate to avoid accumulation everywhere under gates on top Then make 2 layers of plunger/barrier

- Isolating dielectric: AlO from ALD, gates made from Ti/Pd, SiGe/Si/SiGe heterostructure for 2DEG
- Read-out with charge sensors+tank circuit

### Initialization/Measurements:

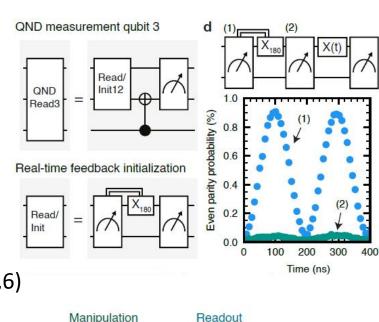
g

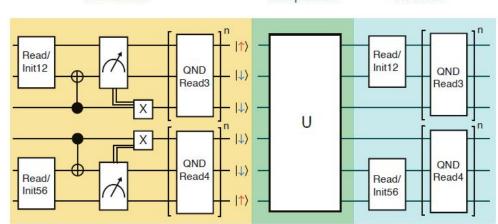


Spin-blockade read-out -> get parity between outer two dots (1,2 or 5,6)

Initialization

е

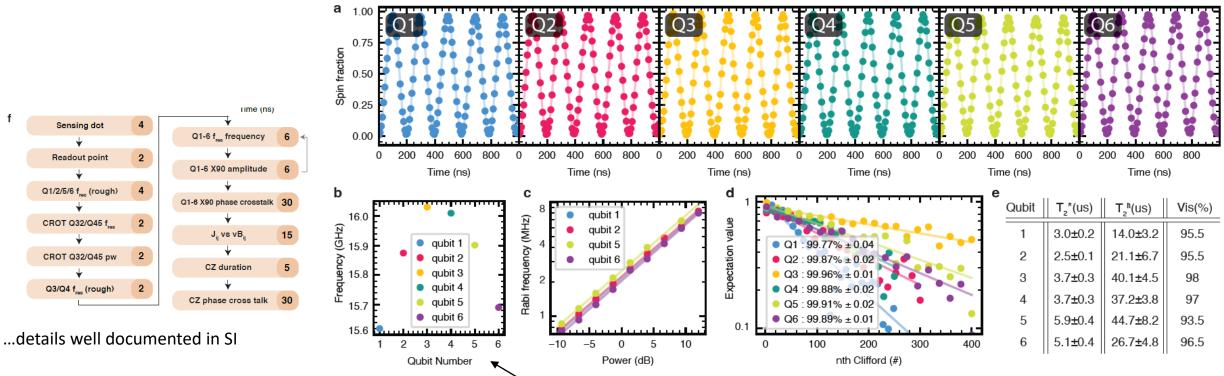




#### Consider level spacing assuming $E_{orb} > E_{valley} > E_z$ $V_1$ , up, or $b_1$ V<sub>1</sub>,down,orb<sub>1</sub> (3,1)/(4,0)E<sub>valley</sub> $V_0$ , up, or $b_1$ V<sub>0</sub>,down,orb<sub>1</sub> Larger E<sub>orb</sub> "read-out window" V<sub>1</sub>,up,orb<sub>0</sub> V<sub>1</sub>,down,orb<sub>0</sub> V<sub>0</sub>,up,orb<sub>0</sub> $E_7$ V<sub>0</sub>,down,orb<sub>0</sub> **Errors** ZI measurement with ZZ operator IZ measurement with ZZ operator Left was 0 00 00 even ...analogous Left was 0 01 11 even Left was 1 10 10 odd Left was 1 11 01 odd

Why 3,1 -> 4,0:

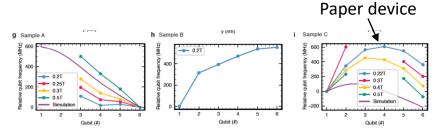
# Calibration routines and single qubit benchmarking



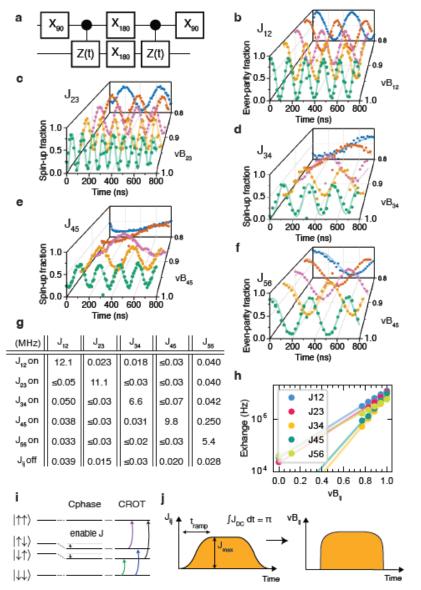
They calibrate every day everything but crosstalk

They do full calibration 2xweek And specific calibrations when needed Note that qubit frequencies are monotonically increasing... The magnet design is discussed in the SI (including simulations etc), but this looks a bit random

(including simulations etc), but this looks a bit random... Have we spotted a weakness in the shiny armour of the Vandersypen group?

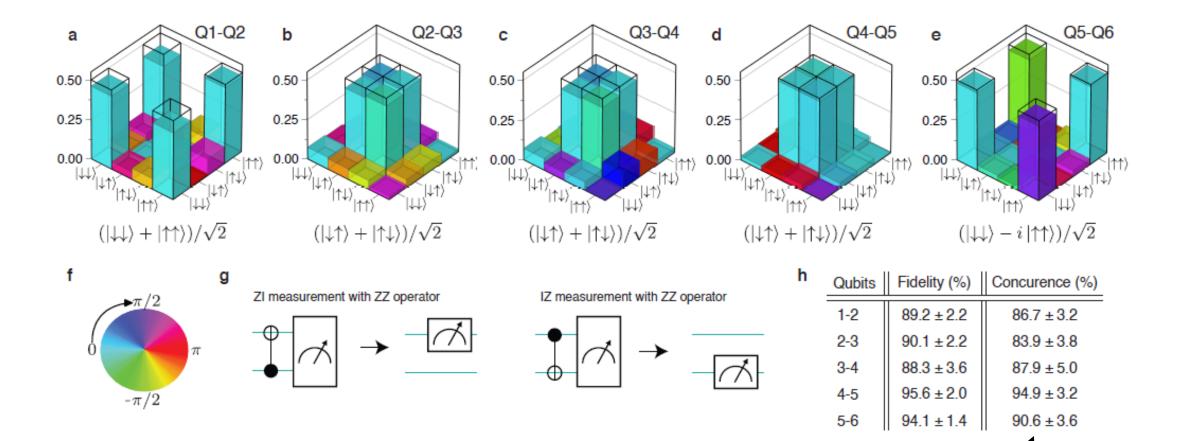


## Two qubit gates



- Use Crot gate:
  - -> pulse target on equator
  - -> Switch exchange on via barrier gate for time T
- Important: T and J need to be chosen such that the phase accumulated on target is pi if control was up Only then we call this Crot
- Pulse target back (should be 180 degrees rotated if control was up)
- Trick 1: Here they also use the barriers on the sides of the pair to push the wavefunctions closer together and increase J
- Trick 2: Use a smooth pulse on barrier (not rectangular pulse) to ensure adiabatic operation

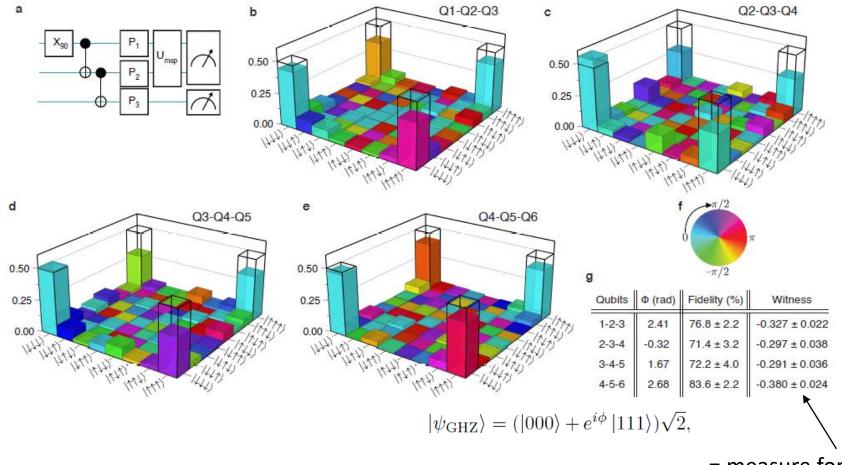
### Characterize two qubit: Bell-state tomography



Why not rdm benchmarking? -> Possible but costly...

= measure for entanglement!

### Characterize three qubit: GHZ states



= measure for entanglement!

### Final comments:

- Probably the most advanced experiment in spin-qubits
- They mention issues with parallelization of gates/initialization (here they do everything consecutively, e.g. 1,2,3 and 4,5,6 initialization)
- They suspect there is an issue with heating... they see that in charge sensor and in the qubit performance
- I think they cannot control their magnets very well
- Valleys also seem to be an issue especially when scaling up... bonus point for holes I would say!
- I encourage every new student to really try and understand the concepts here it has almost everything you need and it is well documented in the SI/extended data On that note, also check out this great review from last week on reflectometry (almost textbook level detailed explanation):
  Probing quantum devices with radio-frequency reflectometry
  Prob
  - <sup>3)</sup>ARC Centre of Excellence for Engineered Quantum Systems, School of Physics, The University of Sydney, NSW 2006, Australia
  - <sup>4)</sup>Microsoft Quantum Sydney, The University of Sydney, Sydney, NSW 2006, Australia
  - <sup>5)</sup>Quantum Motion Technologies, Windsor House, Cornwall Road, Harrogate, HG1 2PW, United Kinedom
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(Dated: 23 February 2022)