Exchange control in a MOS double quantum dot made using a 300 mm wafer process

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Motivation

Leveraging the advanced manufacturing capabilities of the semiconductor industry promises to help scale up silicon-based quantum processors by increasing yield, uniformity and integration. Recent studies of quantum dots fabricated on 300 mm wafer metal-oxide-semiconductor (MOS) processes have shown control and readout of individual spin qubits, yet quantum processors require two-qubit interactions to operate. Here, we use a 300 mm wafer MOS process customized for spin qubits and demonstrate coherent control of two electron spins using the spin-spin exchange interaction, forming the basis of an entangling gate such as \sqrt{SWAP} . We observe gate dephasing times of up to $T_2^* \approx 500$ ns and a gate quality factor of 10. We further extend the coherence by up to an order of magnitude using an echo sequence. For readout, we introduce a dispersive readout technique, the radiofrequency electron cascade, that amplifies the signal while retaining the spin-projective nature of dispersive measurements. Our results demonstrate an industrial grade platform for two-qubit operations, alongside integration with dispersive sensing techniques.

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Dumoulin Stuyck et al., *Symposium on VLSI Circuits*, 1-2, (2021)
Elsayed et al. *arXiv*: 2212.06464, (2022)
Diraq, press release





Background: DC-PSB Cascade Readout



 P_3

(1110)

 P_4

(0201)

(1101)

 P_1

(1200)

(1101)

 P_2

T(0200)

S(0201)

S(0200)



Spin-to-charge
conversionCascadeCharge
sensing $|T\rangle$
(e)(e)(...
(e)(...
(e)(...
(e) $|S\rangle$
(e)(...
(e)(...
(e)(...
(e)

Readout SNR:

- Boosted by 3.4 over "classical" PSB at $1 \leftrightarrow 2$
- Due to closer proximity of charge relocation

Sensor far away from qubit:

- GaAs 4-dot
- PSB at dots $1 \leftrightarrow 2$
- Cascaded charge relocation
- Strong sensor signal at dot 4

The Device & Setup



IMEC-device (planar, high-R Si):

- 30 nm thick poly-Si gates (EBL)
- 8 nm SiO₂ (thermal) + 5 nm (high-T) gate-oxide
- 2 gates but form a triple-dot ("corner-dots" similar in FinFETs?)

Reflectometry setup:

- Superconducting spiral inductor in parallel with **ohmic contact**
- Sensitive to reservoir transitions of large dot \mathbf{Q}_{ME}







Making Sense of the Stability Map (an Attempt)





Observe jumps in sensor transition:

- "2" slopes -> Q₁ & Q₂
- Charge configuration of Q₁ & Q₂ known (not shown explicitly)
- Vary G_s voltage
- Faint feature at interdot transition (1,1,N) ↔ (0,2,N-1)
- "Electron Cascade" because it is a 2nd order-process

The Cascade Q_1 Q_2 Q_{ME} 0.5 $\Delta \Phi / \Phi_0$ 600 1.10 -(02N) (1,1,N) V_{rf} (11N) B (mT) (02N-1 1.09 -(11N) (1,2,N-1) 3 V_G, (V) 1.08 -0. 0.1.1 3 1.07 -(1,2,N) (0,1,N-1) (0,2,N-1) (02N) 0,2,1 V_{rf} 1.06 -(02N-1) (11N) $(N_{Q_1}, N_{Q_2}, N_{Q_{ME}})$ (11N) 0.69 0.67 0.68 0.70 0.71 $V_{G_2}(v)$

RF excitation of tank capacitively¹ "drags" the level of Q₂ across the interdot transition in a cycle -> Larger dispersive shift than through in-situ dispersive charge readout!

-> SNR amplification:
$$A = 1 + \frac{1 - \alpha_{r,ME}}{\alpha_{r,2} - \alpha_{r,1}} > 1$$

Non-demolition RO!

Detecting PSB (the Quantum Motion-way...)



Model yields $t_c = 2.4 \ GHz \ T_e = 50 \ mK$

Singlet-Triplet Oscillations





Liles et al., Nat. Comms, 15, 7690 (2024)
Jirovec et al., Nat. Mater, 20, 1106-1112, (2021)



Splitting of S and T₀: $h\Omega = \sqrt{J(\varepsilon)^2 + \Delta E_Z^2}$

Spin detuning $\Delta E_Z = \Delta g \mu_B B + g \mu_B \Delta B_{HF}$



Applied field: 250 mT vs. Holes in planar Si:^[1] 5 mT vs. Holes in planar Ge/SiGe:^[2] 0.5 mT

Hyperfine energy: 3.4 neV

Spin-Orbit Interaction



 $\Delta E_Z = \Delta g \mu_B B + g \mu_B \Delta B_{HF}$

Variations of Δg with in-plane field angle:

- Change in S-T oscillation frequency
- Model based on Rashba and Dresselhaus SOI:

$$\Delta E_z/h = \sqrt{\left(|B| \left|\Delta \alpha - \Delta \beta \sin(2\phi_B)\right|\right)^2 + \left(g\mu_B \sigma_{\rm HF}/h\right)^2}$$



 $\Delta \alpha = 6.2^{+1.6}_{-1.5} \text{ MHz/T} \ \Delta \beta = 45^{+5}_{-7} \text{ MHz/T}$

For comparison, easily achievable for holes in FinFETs and GeSi NWs:

 $\Delta g = 1 \leftrightarrow 14 \ GHz/T$

Exchange Control & Coherence



Really that low Charge/Detuning Noise ???



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Echo Sequence



Conclusions

RF-Electron Cascade Readout:

- Cool experiment, but only works for very specific tuning of dots
- Expands on single-lead dispersive charge sensor
- No single shot or readout fidelity reported

Singlet-Triplet qubit:

- Industrial device fabricated on 300 mm process
- Competitive coherence times and noise levels
- Not enough tunability (need more gates)
- Possibly longer range electron cascade experiments in the future...

Thanks for your attention ③